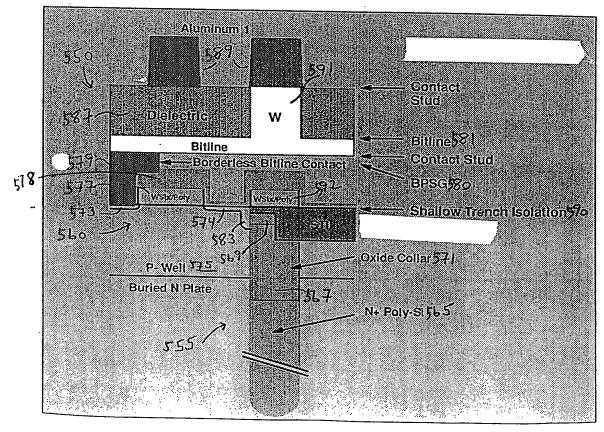
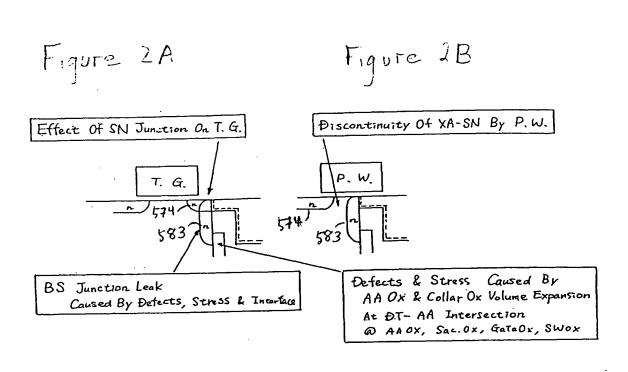


Figure 1B





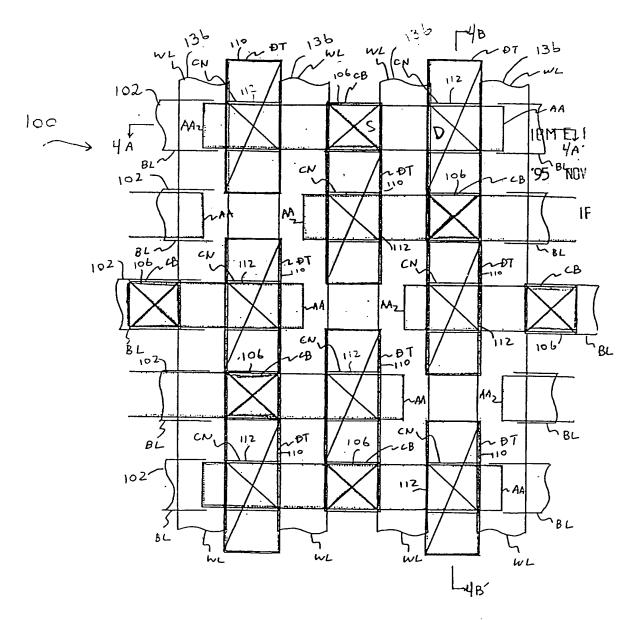
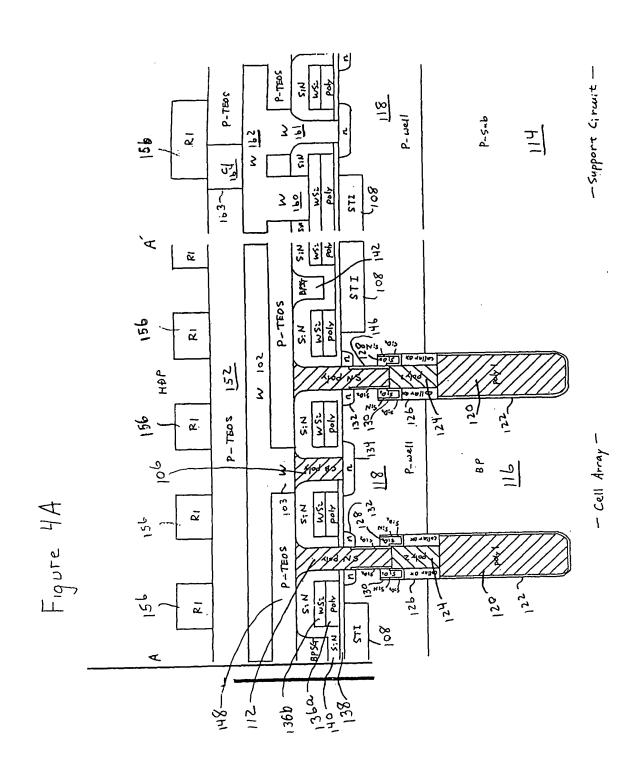


Figure 3



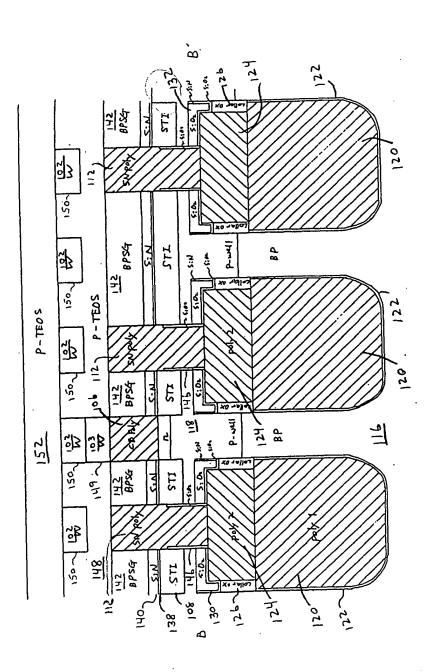
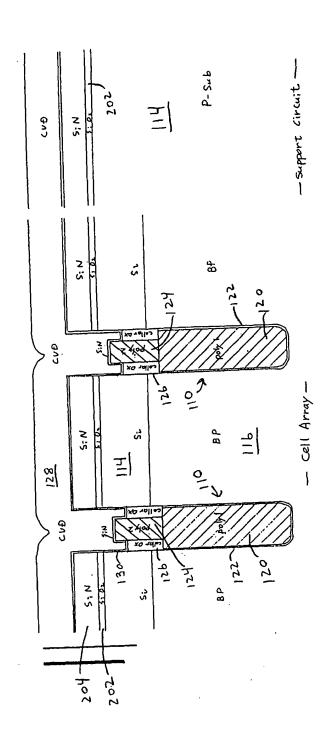
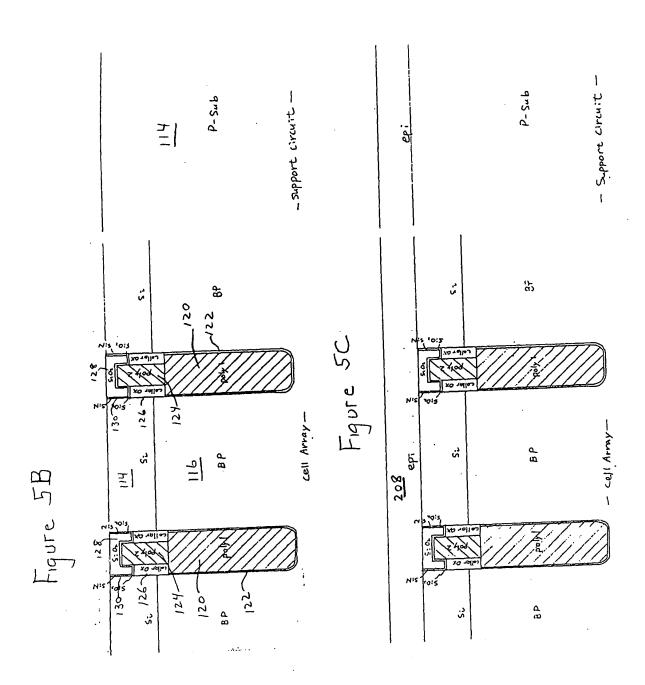


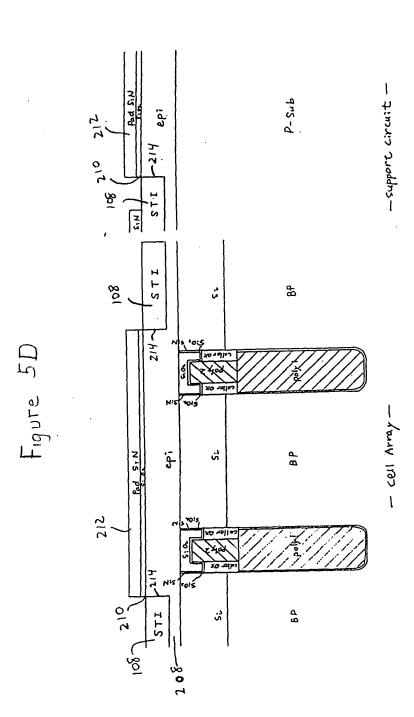
Figure 4B

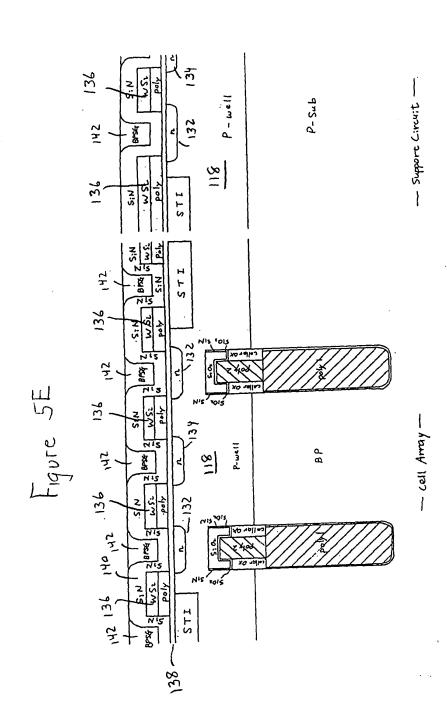
HOP

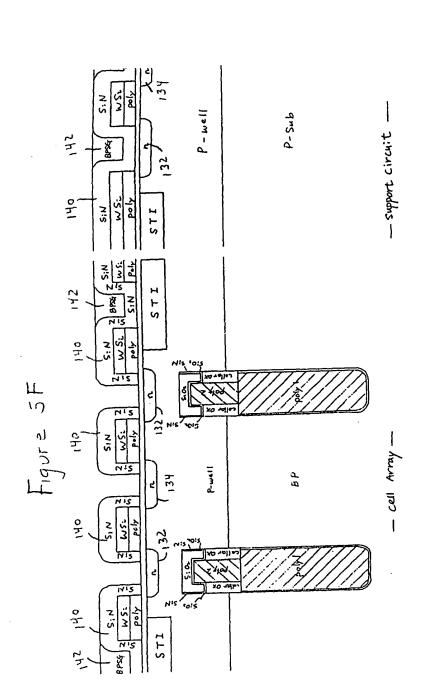
Figure 5A

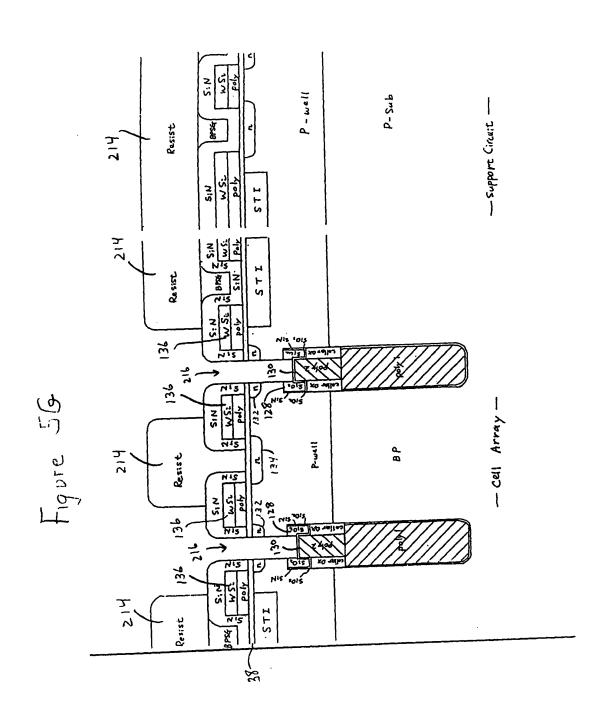






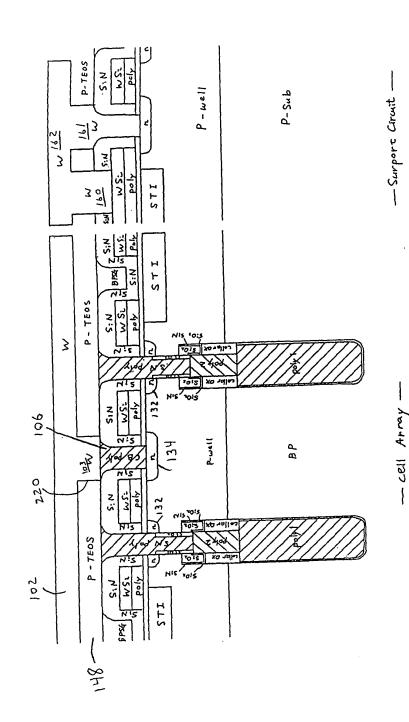






-Support Circuit -P-well P-Sub Sin Sin 4 od STI Figure 5H - Cell Array -10 P-well ВР 218 STI

Figure SI



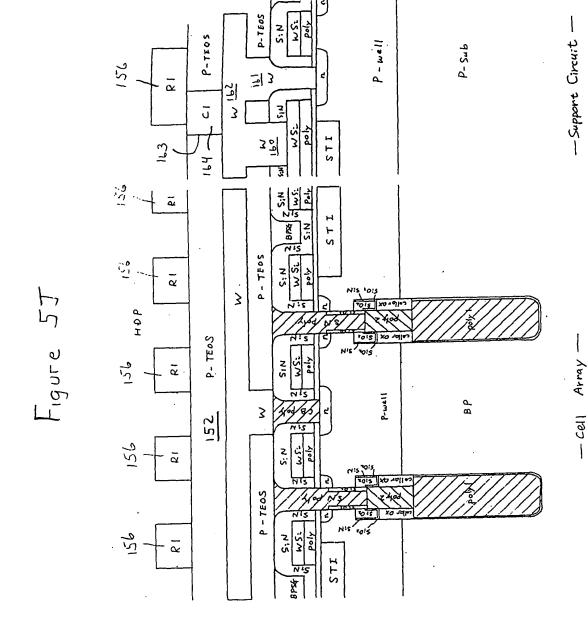
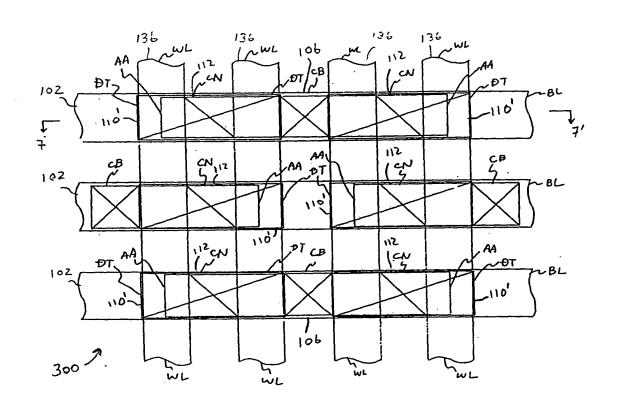


Figure b



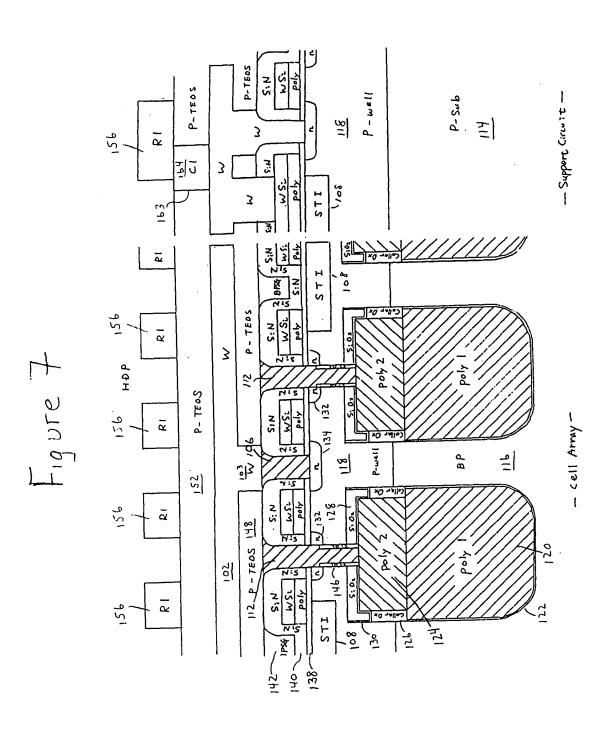


Figure 8

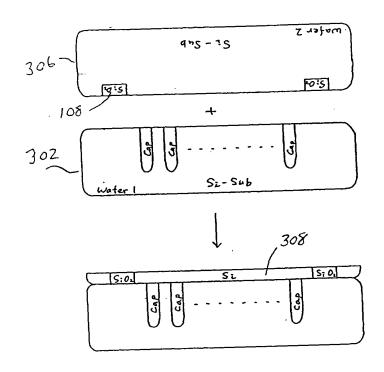
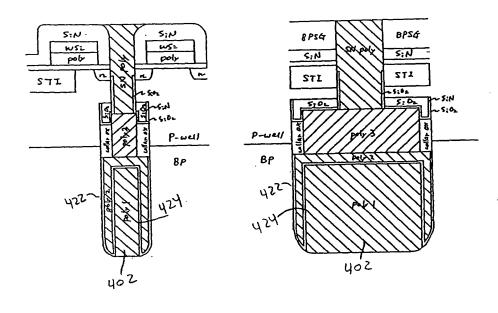


Figure 9A

Figure 9B



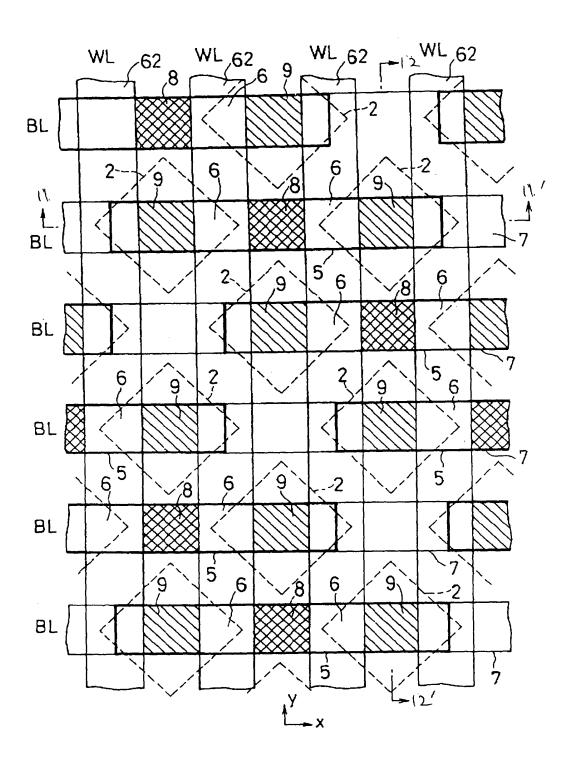


Figure 10

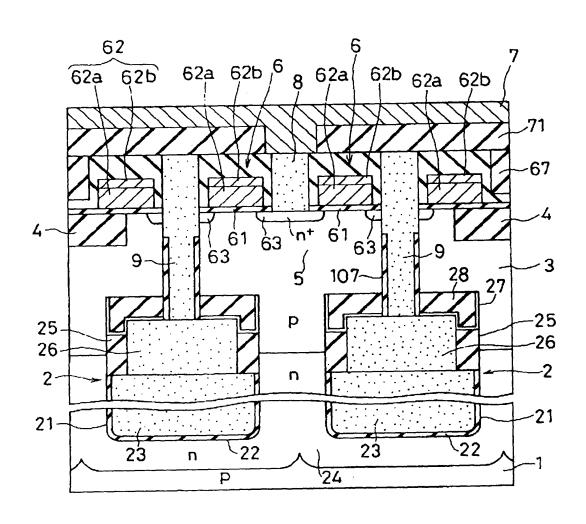
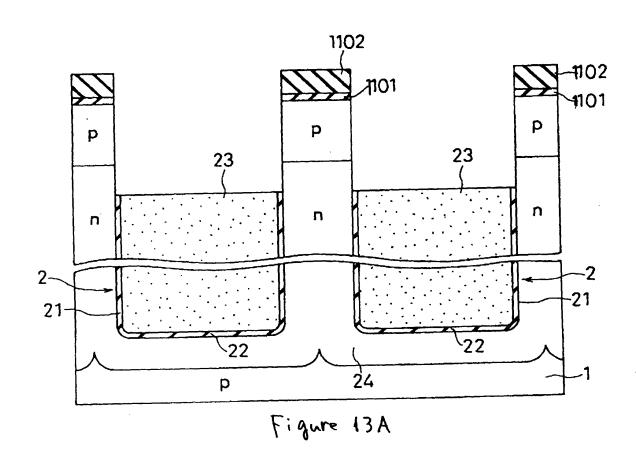


Figure 11



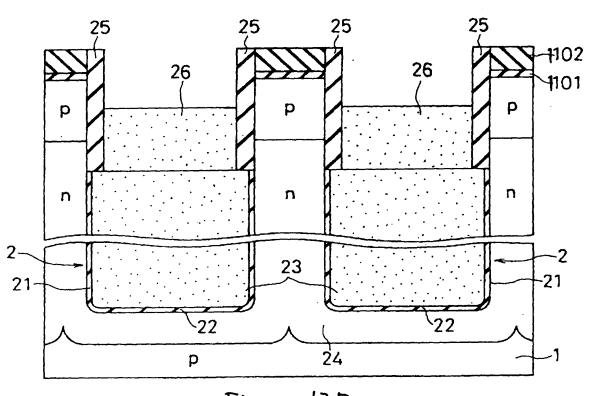


Figure 13B

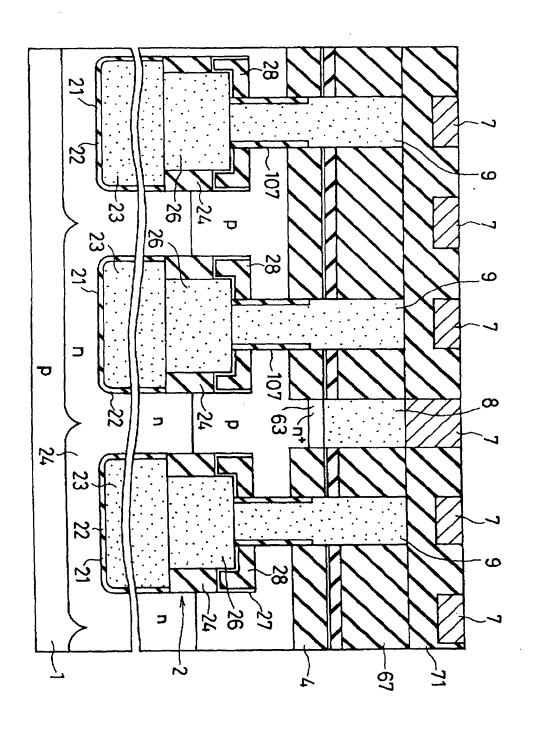


Figure 12

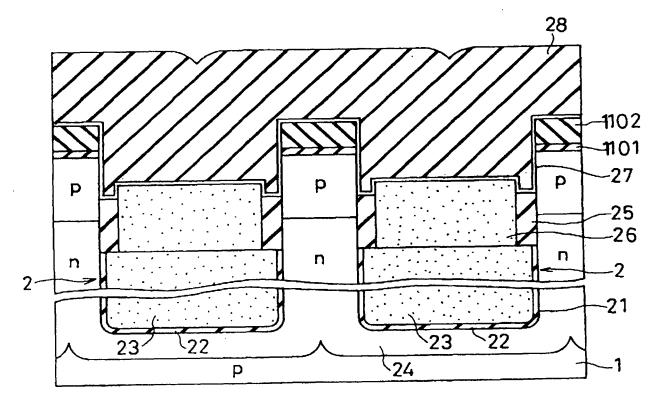


Figure 13C

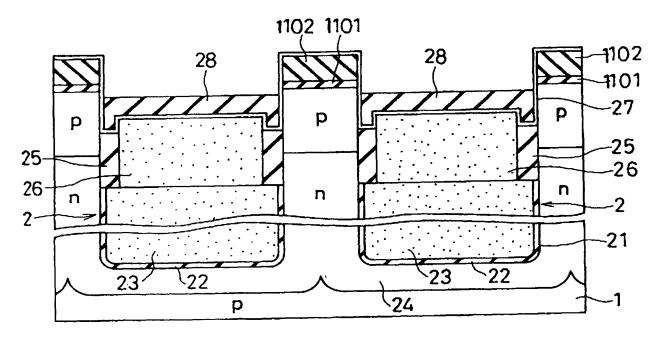


Figure 13D

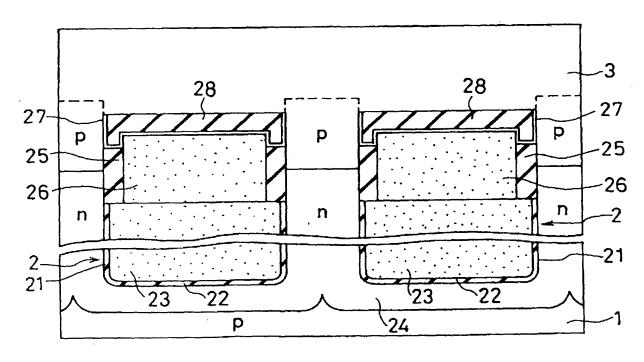


Figure 13E

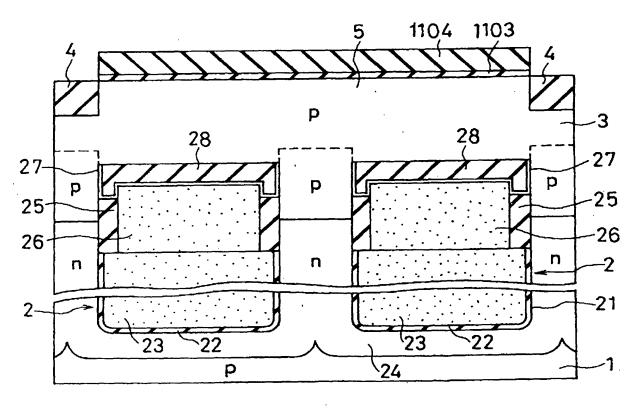
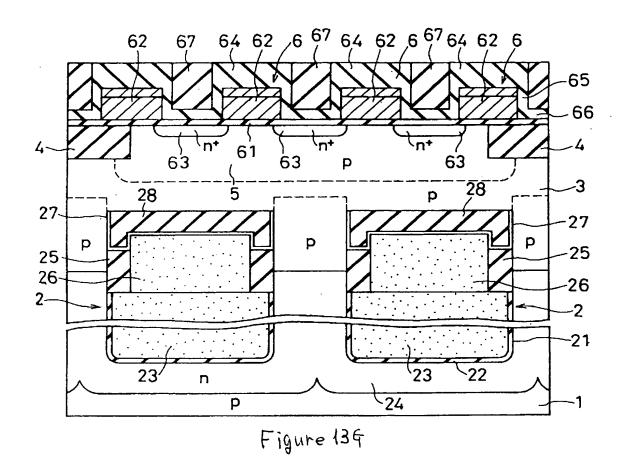
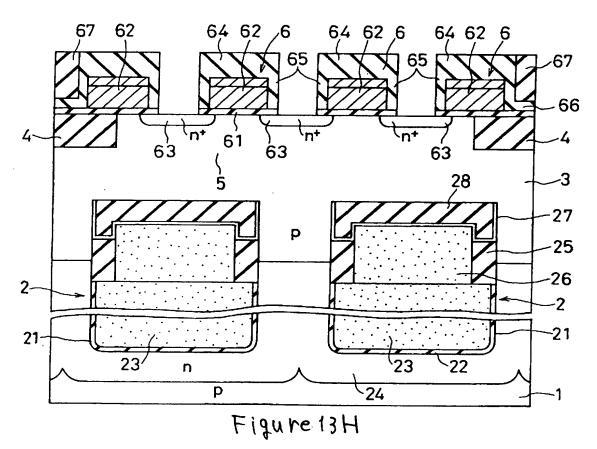
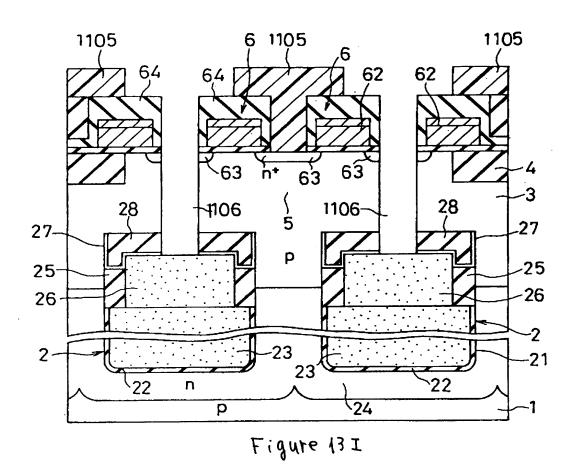


Figure 13F







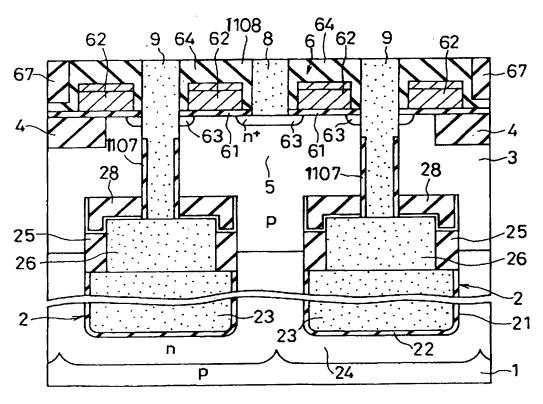


Figure 13J

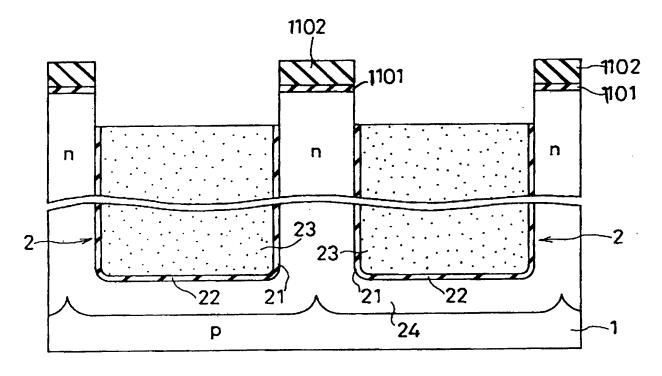


Figure 14A

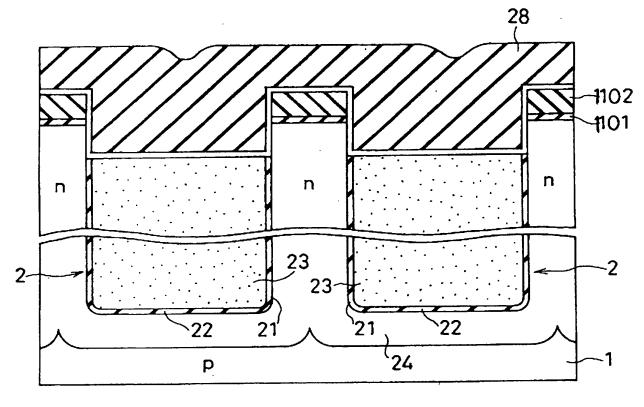
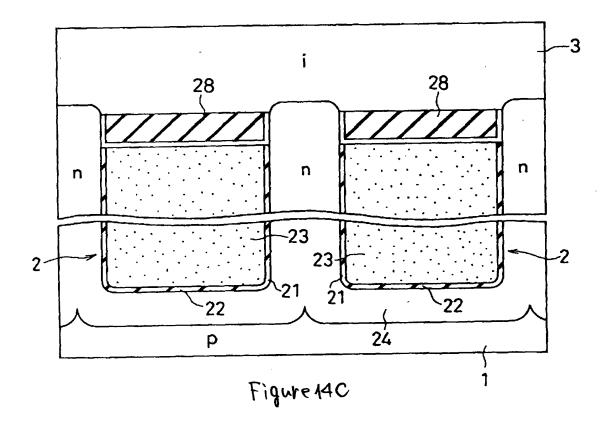


Figure 14B



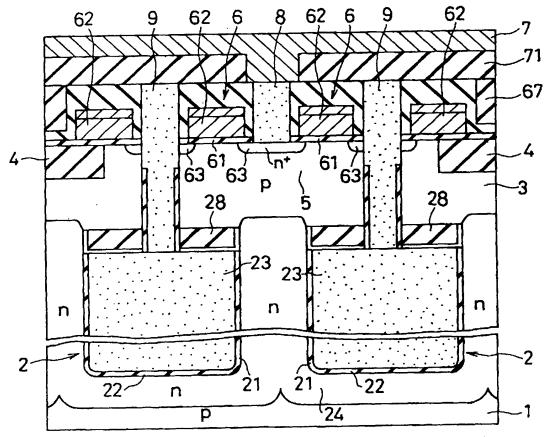


Figure 14D

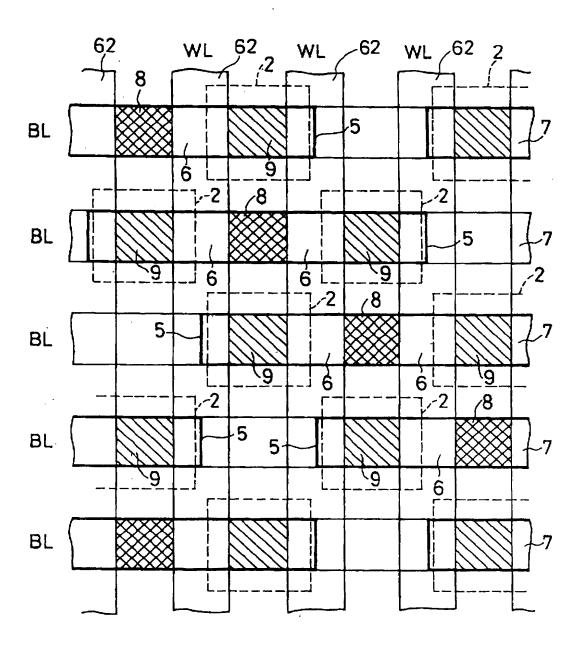
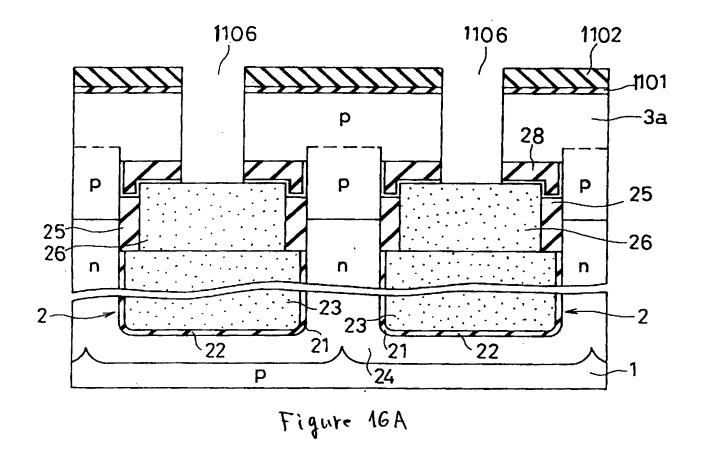


Figure 15



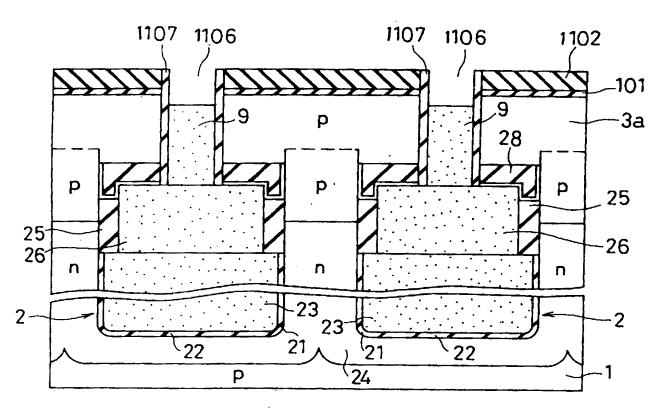


Figure 16B

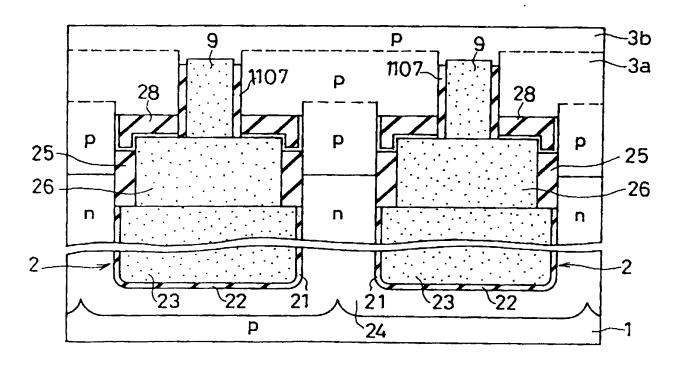


Figure 16C

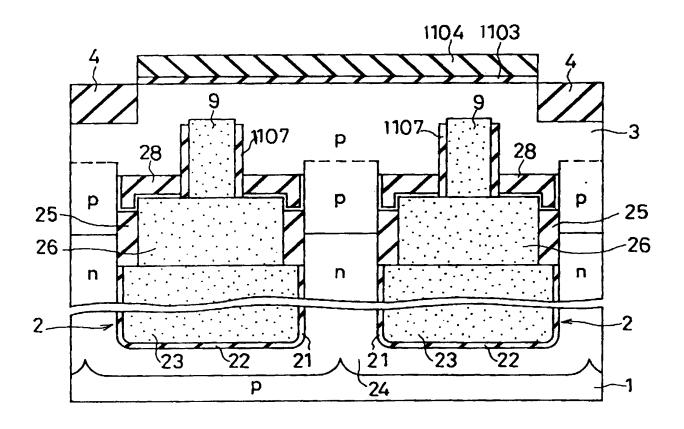


Figure 16D

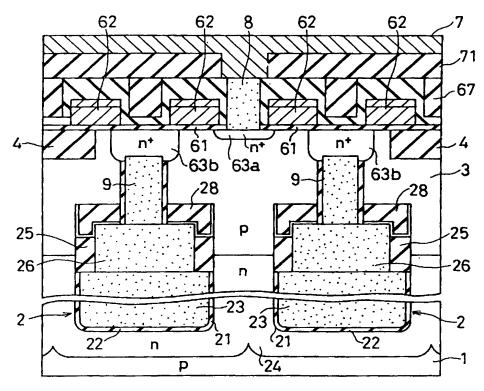


Figure 16E

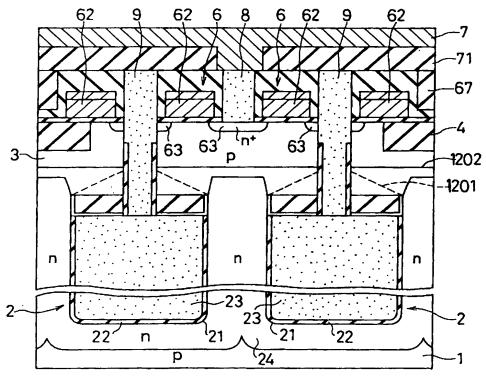


Figure 17

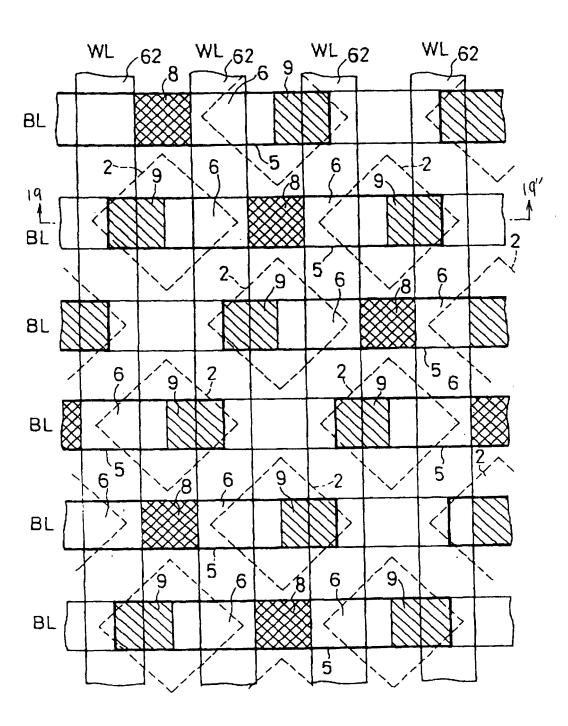
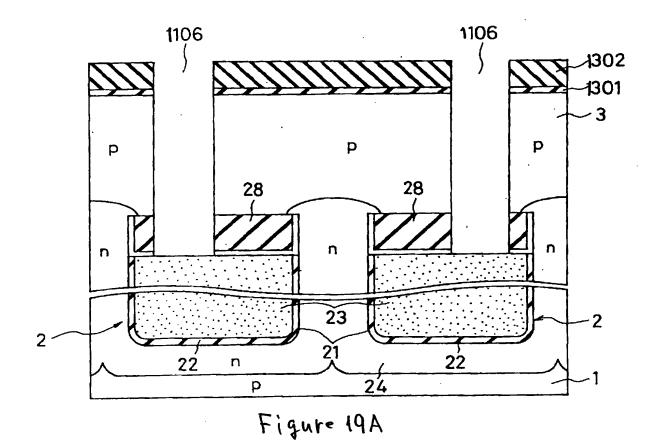
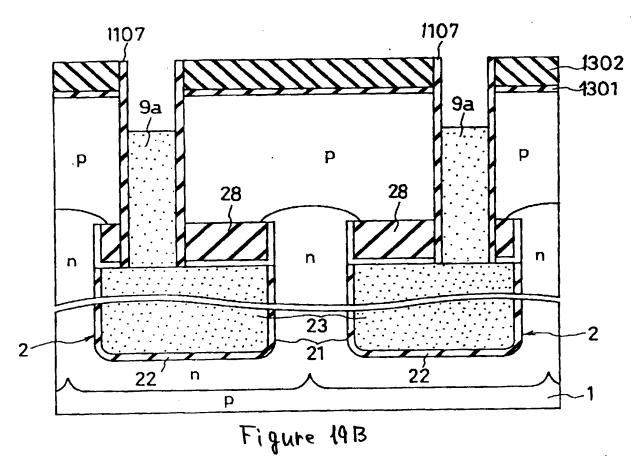
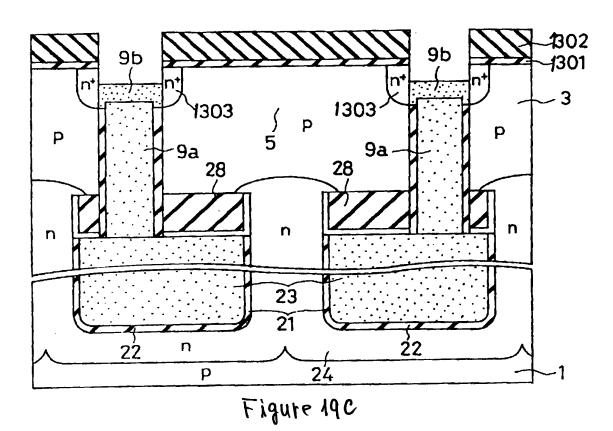
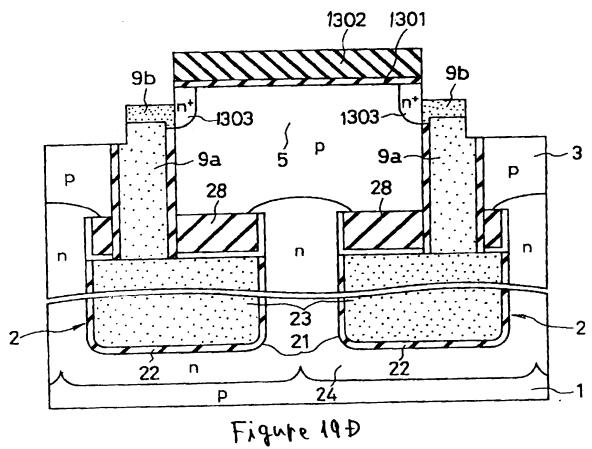


Figure 18









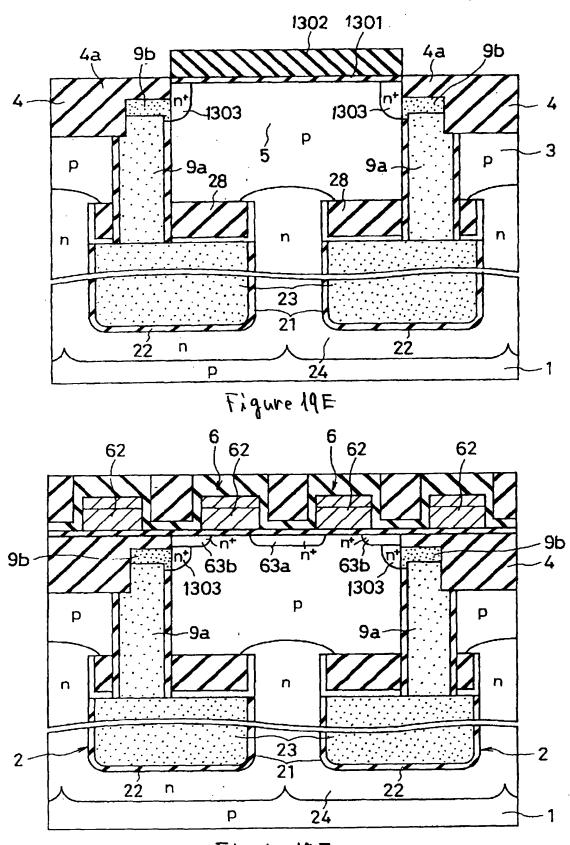


Figure 19F

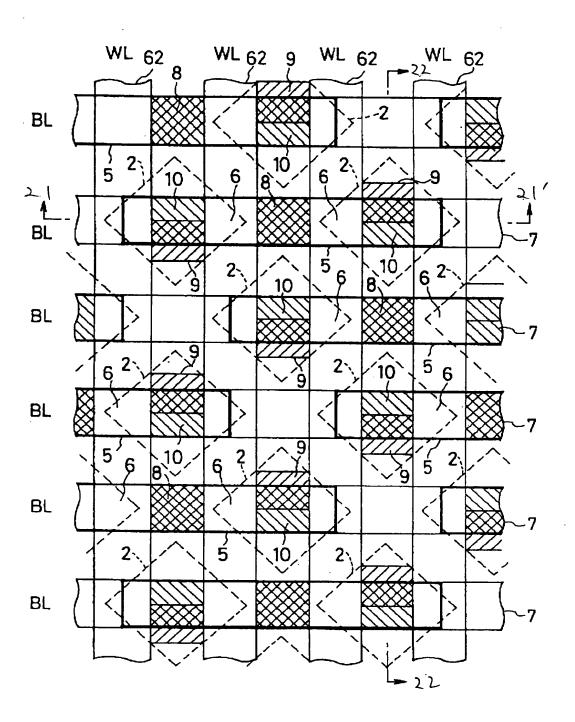


Figure 20

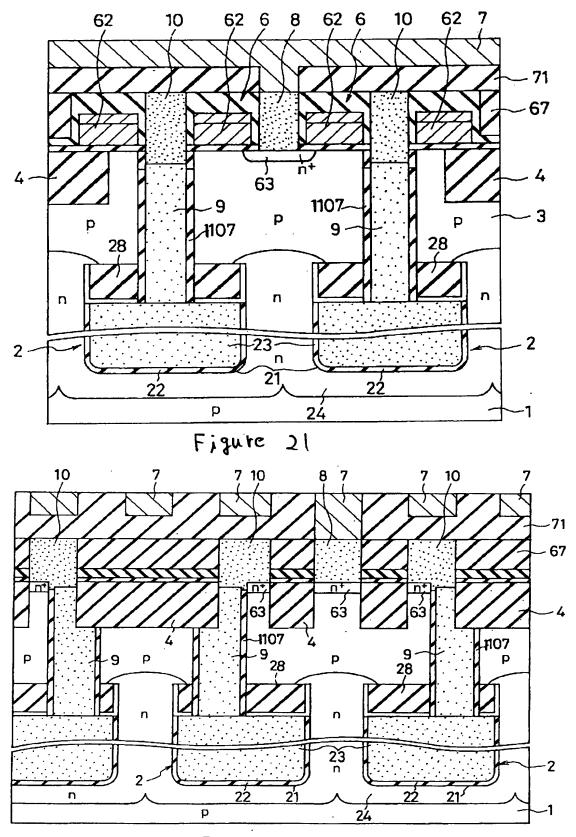
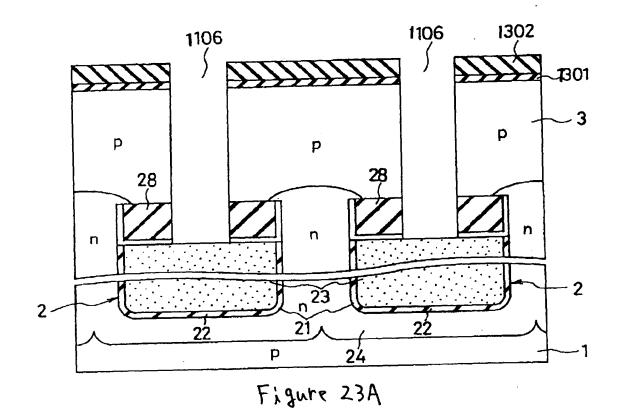
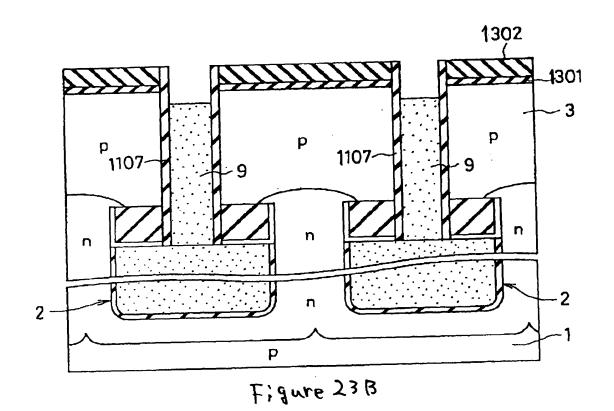
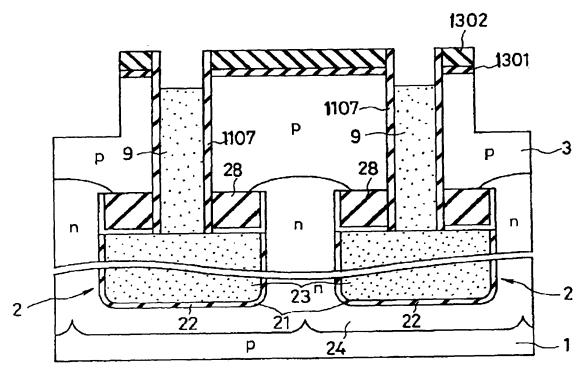


Figure 22







Flance 23C

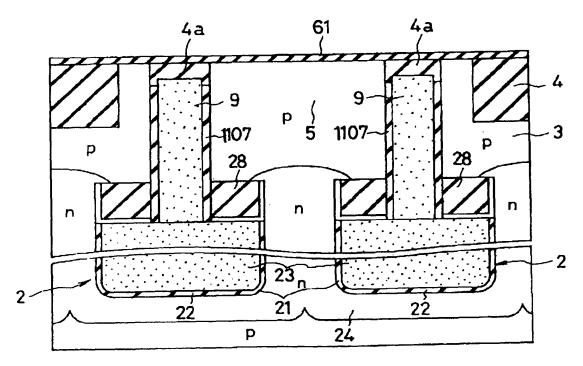
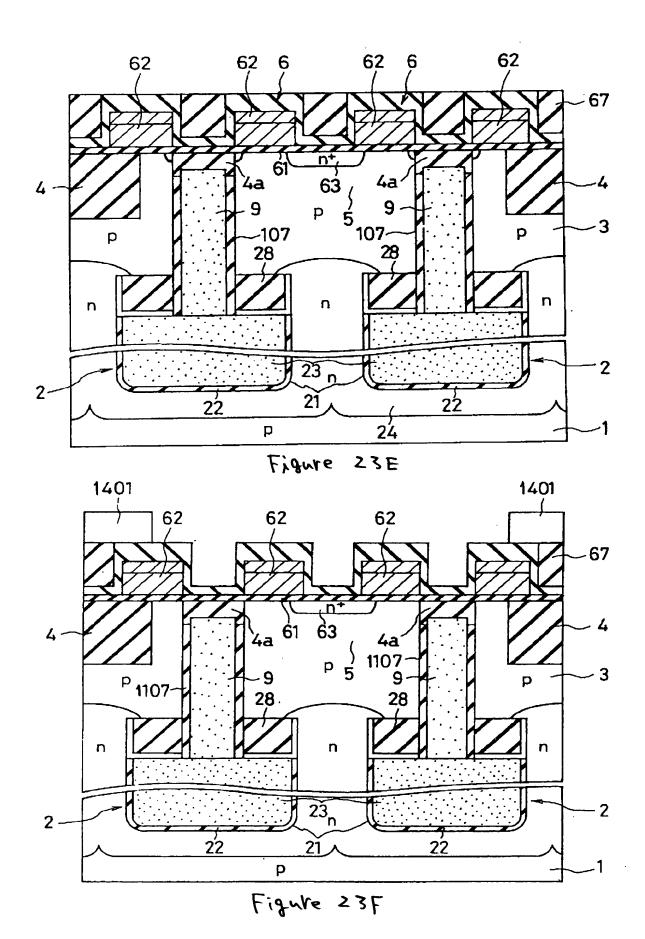
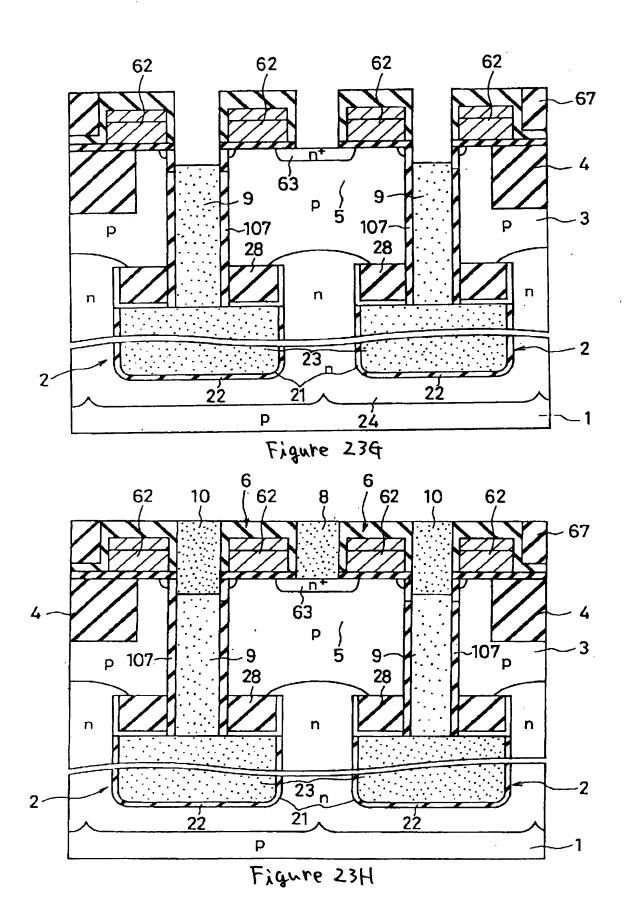


Figure 23D





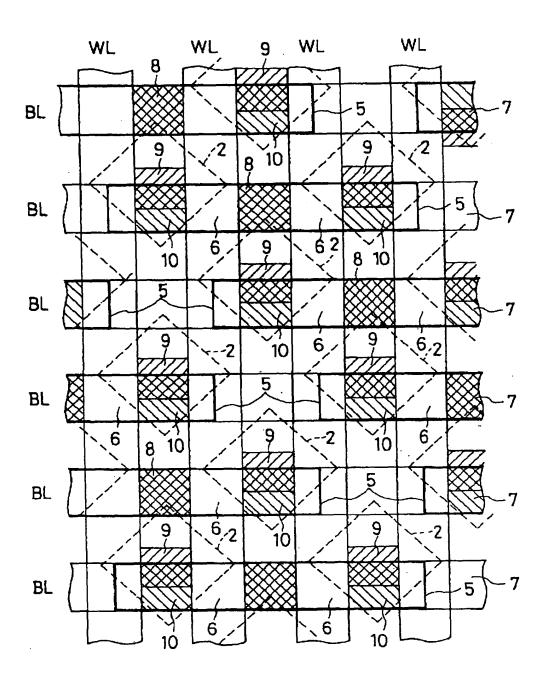


Figure 24

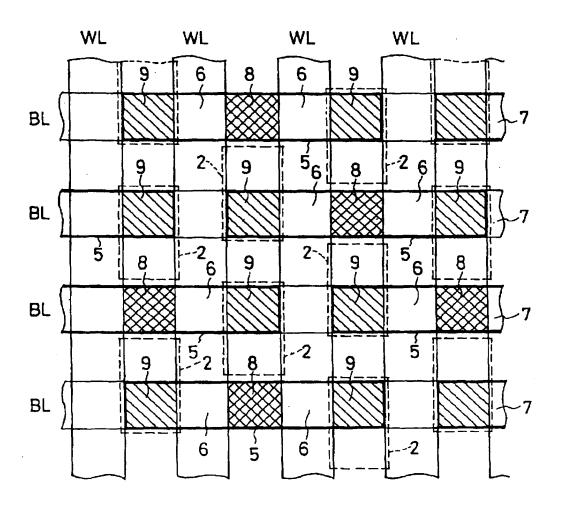
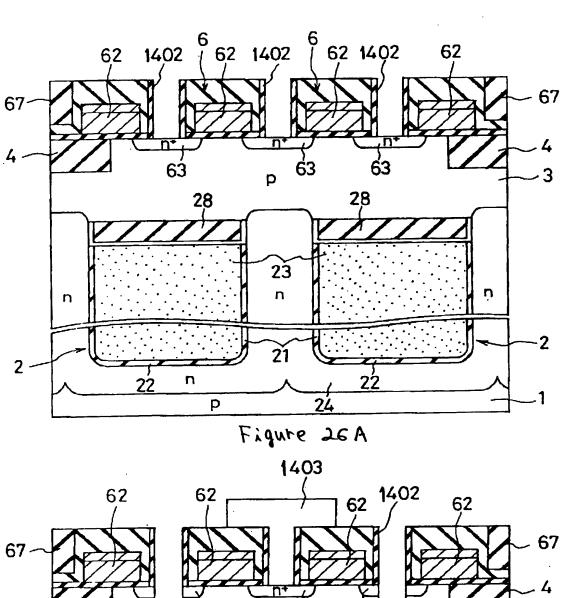


Figure 25



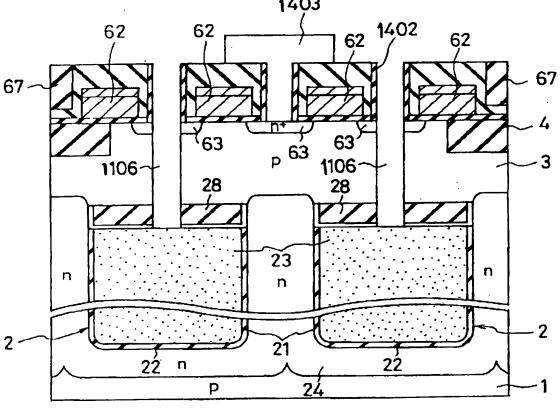
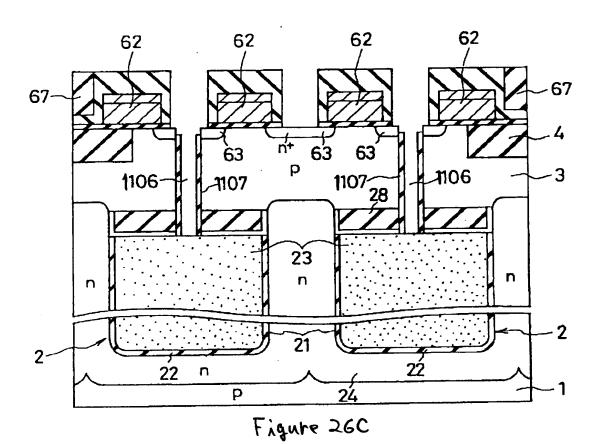


Figure 26B



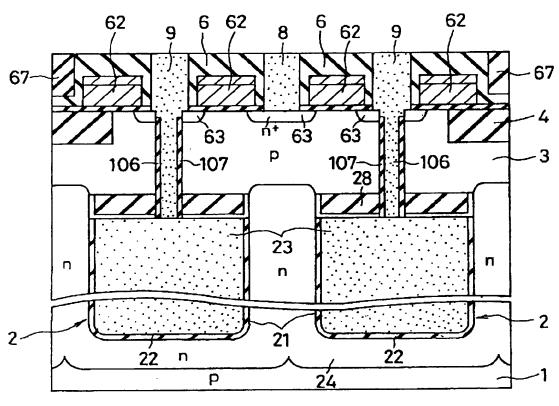


Figure 26D

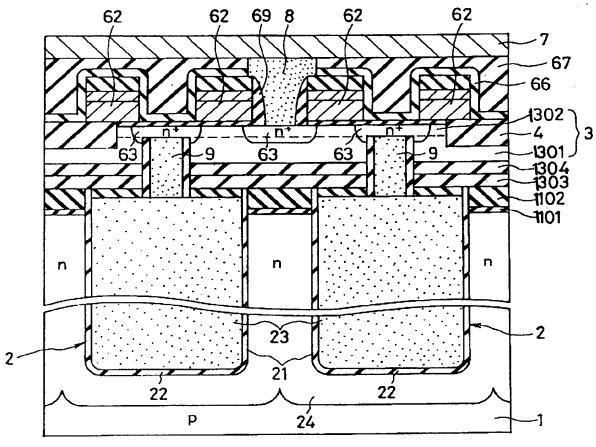


Figure 27

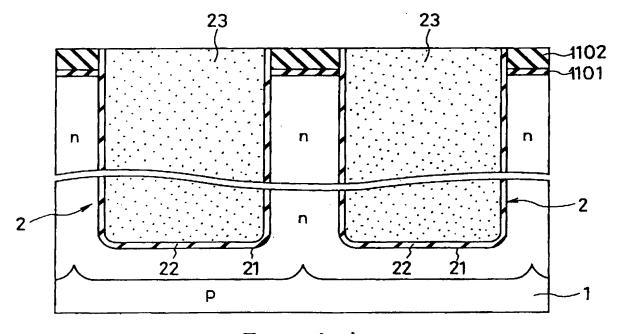
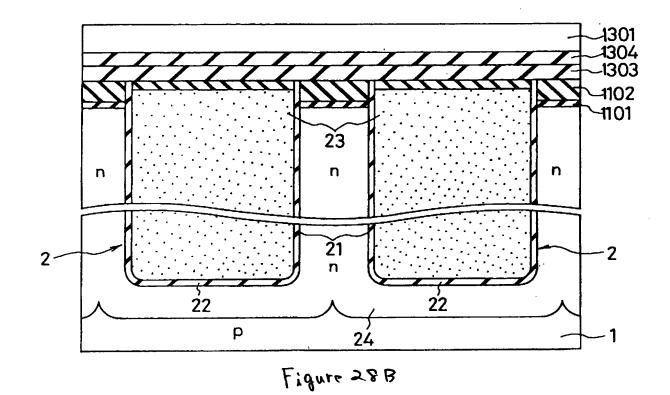
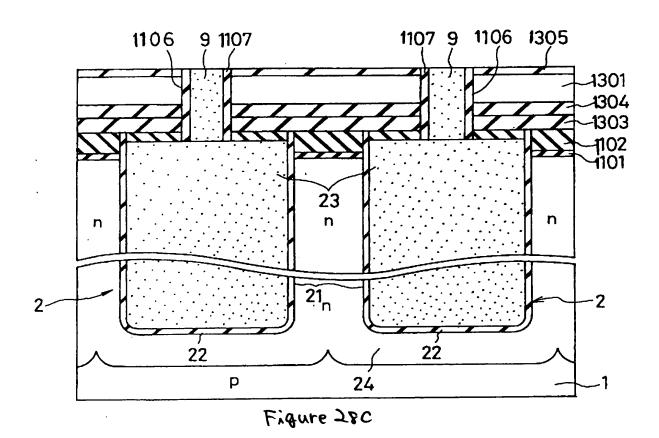


Figure 28A





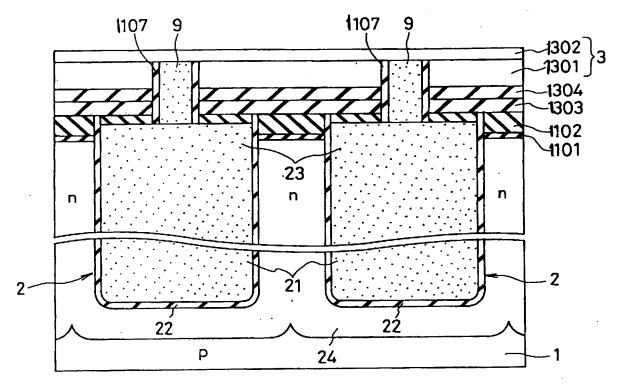


Figure 28D

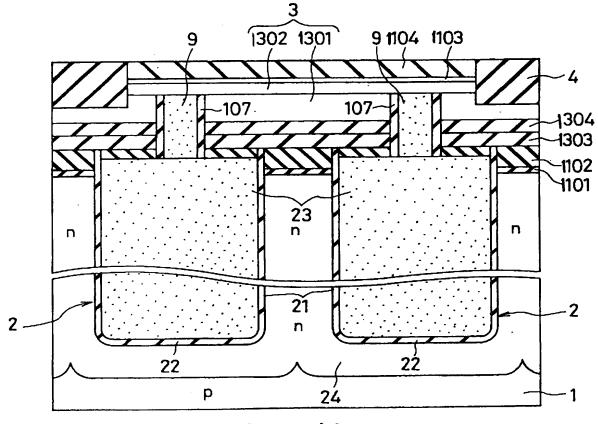


Figure 28E

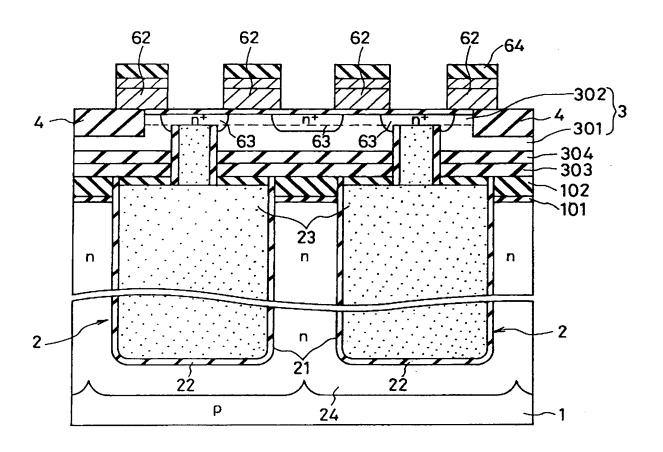


Figure 28F

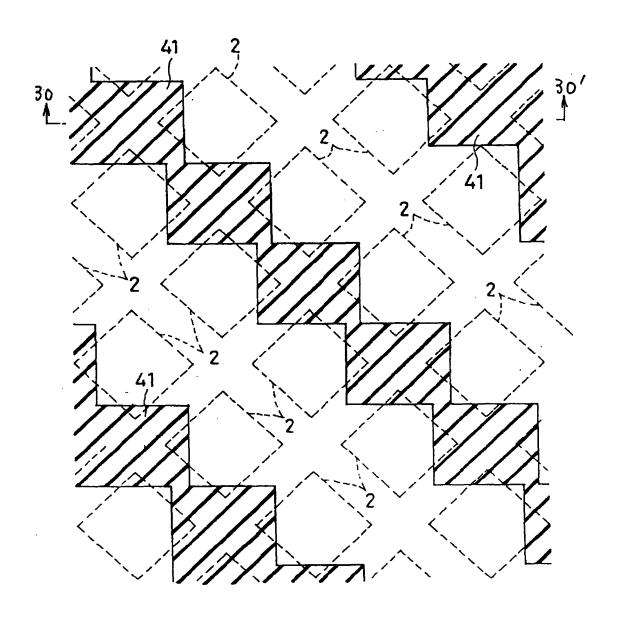


Figure 29

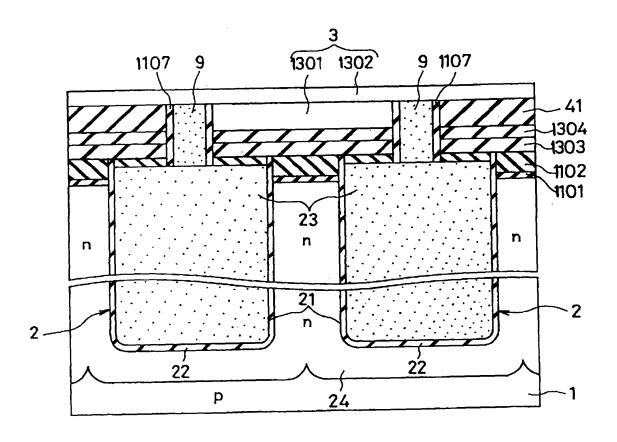


Figure 30

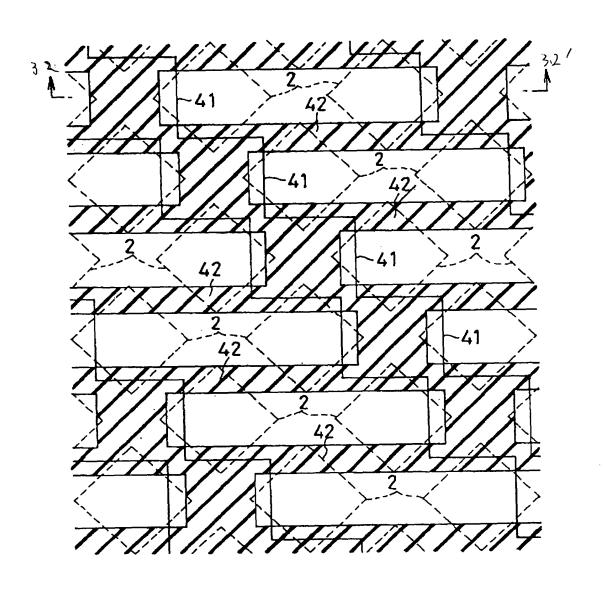
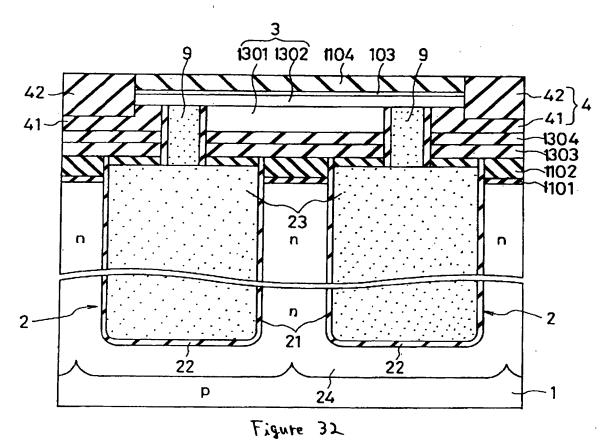


Figure 31



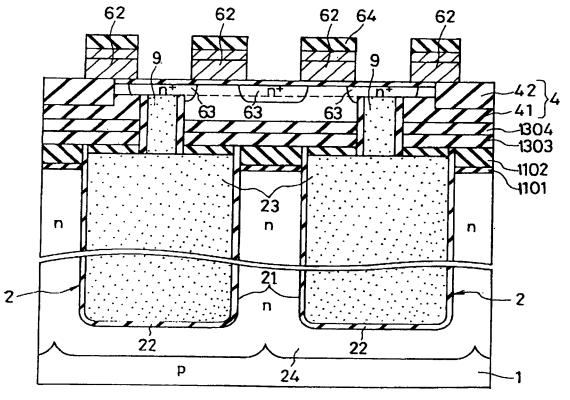


Figure 33